

$V_{DRM}$	= 1800	V
$I_{T(AV)M}$	= 932	A
$I_{T(RMS)}$	= 1464	A
$I_{TSM}$	= $13.7 \times 10^3$	A
$V_{(T0)}$	= 0.936	V
$r_T$	= 0.341	mW

# Phase Control Thyristor

## 5STP 09D1801

Doc. No. 5SYA1058-01 March 05

- Low on-state and switching losses
- Designed for traction, energy and industrial applications
- Optimum power handling capability

### Blocking

Maximum rated values <sup>1)</sup>

Symbol	Conditions	5STP 09D1801	5STP 09D1601	5STP 09D1401
$V_{DRM}, V_{RRM}$	$f = 50 \text{ Hz}, t_p = 10 \text{ ms}$	1800 V	1600 V	1400 V
$dV/dt_{crit}$	Exp. to 1210 V, $T_{vj} = 125^\circ\text{C}$	1000 V/ $\mu\text{s}$		

Characteristic values

Parameter	Symbol	Conditions	min	typ	max	Unit
Forward leakage current	$I_{DRM}$	$V_{DRM}, T_{vj} = 125^\circ\text{C}$			70	mA
Reverse leakage current	$I_{RRM}$	$V_{RRM}, T_{vj} = 125^\circ\text{C}$			70	mA

### Mechanical data

Maximum rated values <sup>1)</sup>

Parameter	Symbol	Conditions	min	typ	max	Unit
Mounting force	$F_M$		8	10	12	kN
Acceleration	$a$	Device unclamped			50	$\text{m/s}^2$
Acceleration	$a$	Device clamped			100	$\text{m/s}^2$

Characteristic values

Parameter	Symbol	Conditions	min	typ	max	Unit
Weight	$m$				0.26	kg
Surface creepage distance	$D_s$		25			mm
Air strike distance	$D_a$		14			mm

<sup>1)</sup> Maximum rated values indicate limits beyond which damage to the device may occur

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## On-state

### Maximum rated values <sup>1)</sup>

Parameter	Symbol	Conditions	min	typ	max	Unit
Average on-state current	$I_{T(AV)M}$	Half sine wave, $T_c = 70^\circ\text{C}$			932	A
RMS on-state current	$I_{T(RMS)}$				1464	A
Peak non-repetitive surge current	$I_{TSM}$	$t_p = 10\text{ ms}$ , $T_{vj} = 125^\circ\text{C}$ , $V_D = V_R = 0\text{ V}$			$13.7 \times 10^3$	A
Limiting load integral	$I^2t$				$938.5 \times 10^3$	$\text{A}^2\text{s}$
Peak non-repetitive surge current	$I_{TSM}$	$t_p = 8.3\text{ ms}$ , $T_{vj} = 125^\circ\text{C}$ , $V_D = V_R = 0\text{ V}$			$14.6 \times 10^3$	A
Limiting load integral	$I^2t$				$880 \times 10^3$	$\text{A}^2\text{s}$

### Characteristic values

Parameter	Symbol	Conditions	min	typ	max	Unit
On-state voltage	$V_T$	$I_T = 1500\text{ A}$ , $T_{vj} = 125^\circ\text{C}$			1.45	V
Threshold voltage	$V_{(T0)}$	$I_T = 1000\text{ A} - 3500\text{ A}$ , $T_{vj} = 125^\circ\text{C}$			0.936	V
Slope resistance	$r_T$				0.341	$\text{m}\Omega$
Holding current	$I_H$	$T_{vj} = 25^\circ\text{C}$		170		mA
		$T_{vj} = 125^\circ\text{C}$		90		mA
Latching current	$I_L$	$T_{vj} = 25^\circ\text{C}$		450		mA
		$T_{vj} = 125^\circ\text{C}$		350		mA

## Switching

### Maximum rated values <sup>1)</sup>

Parameter	Symbol	Conditions	min	typ	max	Unit
Critical rate of rise of on-state current	$di/dt_{crit}$	$T_{vj} = 125^\circ\text{C}$ , $I_T = I_{T(AV)}$ , Cont. $f = 50\text{ Hz}$			200	$\text{A}/\mu\text{s}$
Critical rate of rise of on-state current	$di/dt_{crit}$	$V_D \leq 1210\text{ V}$ , $I_{FG} = 2\text{ A}$ , $t_r = 0.3\text{ }\mu\text{s}$ Cont. $f = 1\text{ Hz}$			1000	$\text{A}/\mu\text{s}$
Circuit-commutated turn-off time	$t_q$	$T_{vj} = 125^\circ\text{C}$ , $I_{TRM} = 1500\text{ A}$ , $V_R = 200\text{ V}$ , $di_T/dt = -12.5\text{ A}/\mu\text{s}$ , $V_D \leq 0.67 \cdot V_{DRM}$ , $dv_D/dt = 50\text{ V}/\mu\text{s}$		150		$\mu\text{s}$

### Characteristic values

Parameter	Symbol	Conditions	min	typ	max	Unit
Recovery charge	$Q_{rr}$	$T_{vj} = 125^\circ\text{C}$ , $I_{TRM} = 1500\text{ A}$ , $V_R = 200\text{ V}$ , $di_T/dt = -12.5\text{ A}/\mu\text{s}$		1400		$\mu\text{As}$
Gate turn-on delay time	$t_{gd}$	$V_D = 0.4 \cdot V_{RM}$ , $I_{FG} = 2\text{ A}$ , $t_r = 0.3\text{ }\mu\text{s}$ , $T_{vj} = 25^\circ\text{C}$			2	$\mu\text{s}$

## Triggering

Maximum rated values <sup>1)</sup>

Parameter	Symbol	Conditions	min	typ	max	Unit
Peak forward gate voltage	V <sub>FGM</sub>				12	V
Peak forward gate current	I <sub>FGM</sub>				10	A
Peak reverse gate voltage	V <sub>RGM</sub>				10	V
Mean forward gate power	P <sub>G(AV)</sub>				3	W

Characteristic values

Parameter	Symbol	Conditions	min	typ	max	Unit
Gate-trigger voltage	V <sub>GT</sub>	T <sub>vj</sub> = -40 °C			4	V
		T <sub>vj</sub> = 25 °C			3	
		T <sub>vj</sub> = 125 °C	0.25		2	
Gate-trigger current	I <sub>GT</sub>	T <sub>vj</sub> = -40 °C			500	mA
		T <sub>vj</sub> = 25 °C			250	
		T <sub>vj</sub> = 125 °C	10		150	

## Thermal

Maximum rated values <sup>1)</sup>

Parameter	Symbol	Conditions	min	typ	max	Unit
Operating junction temperature range	T <sub>vj</sub>		-40		125	°C
Storage temperature range	T <sub>stg</sub>		-40		125	°C

Characteristic values

Parameter	Symbol	Conditions	min	typ	max	Unit
Thermal resistance junction to case	R <sub>th(j-c)</sub>	Double-side cooled F <sub>m</sub> = 8...12 kN			32	K/kW
	R <sub>th(j-c)A</sub>	Anode-side cooled F <sub>m</sub> = 8...12 kN			52	K/kW
	R <sub>th(j-c)C</sub>	Cathode-side cooled F <sub>m</sub> = 8...12 kN			83	K/kW
Thermal resistance case to heatsink	R <sub>th(c-h)</sub>	Double-side cooled F <sub>m</sub> = 8...12 kN			7.5	K/kW
	R <sub>th(c-h)</sub>	Single-side cooled F <sub>m</sub> = 8...12 kN			15	K/kW

Analytical function for transient thermal impedance:

$$Z_{th(j-c)}(t) = \sum_{i=1}^n R_{th i} (1 - e^{-t/\tau_i})$$

i	1	2	3	4
R <sub>th i</sub> (K/kW)	13.070	8.030	8.200	2.700
τ <sub>i</sub> (s)	0.4857	0.2162	0.0762	0.0043

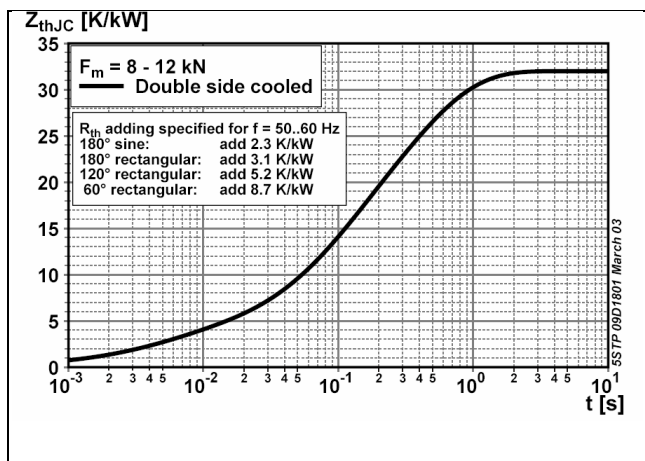


Fig. 1 Transient thermal impedance junction-to case.

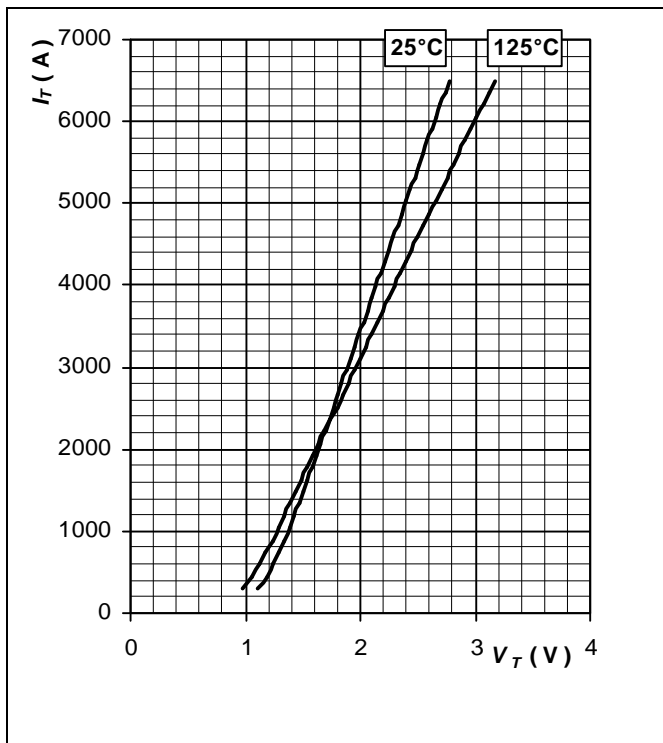


Fig. 2 Max. on-state voltage characteristics

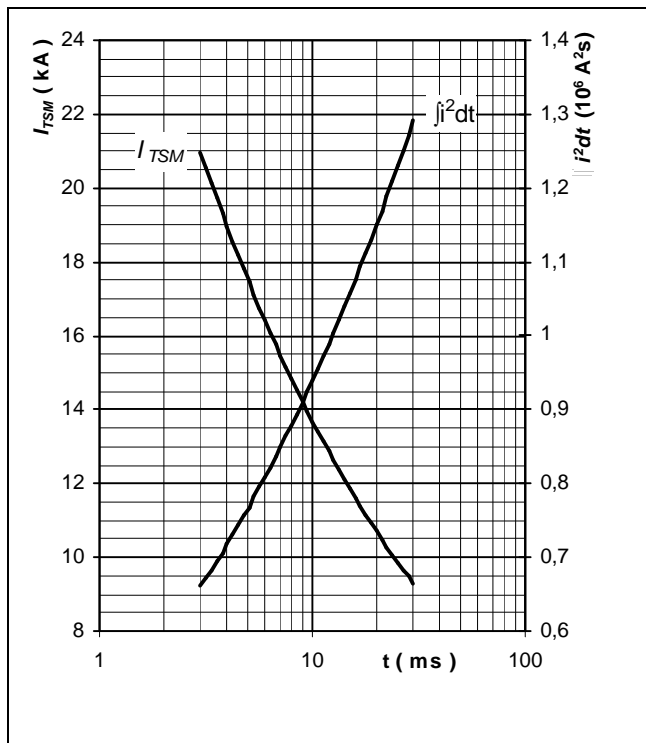


Fig. 3 Surge forward current vs. pulse length. Half sine wave, single pulse,  $V_R = 0$  V

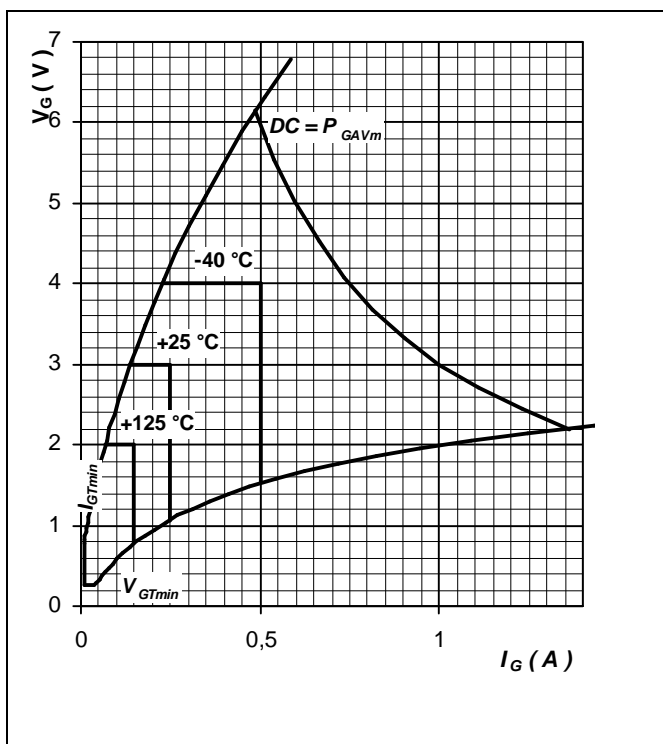


Fig. 4 Gate trigger characteristics

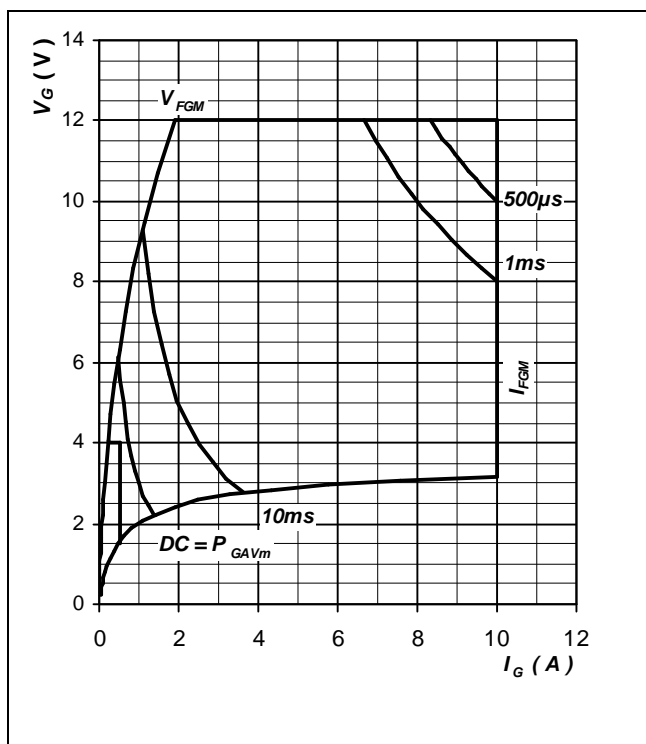
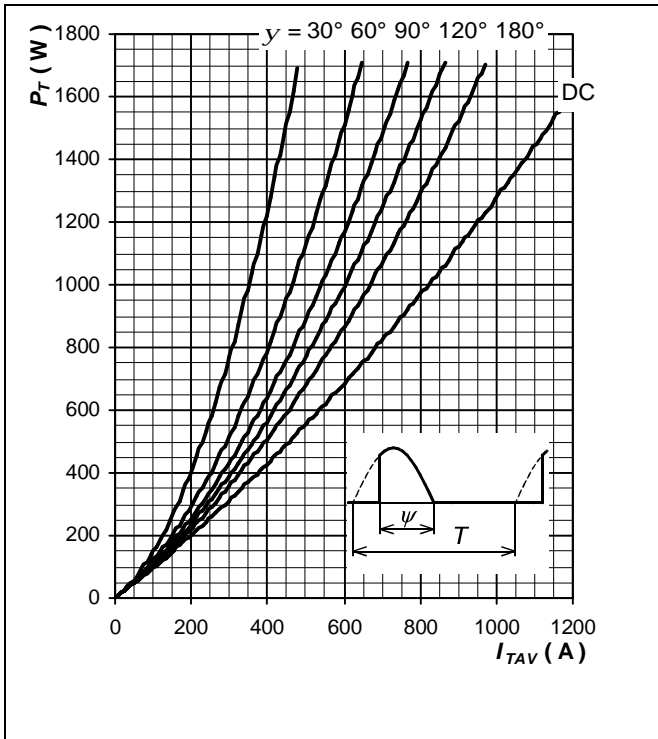
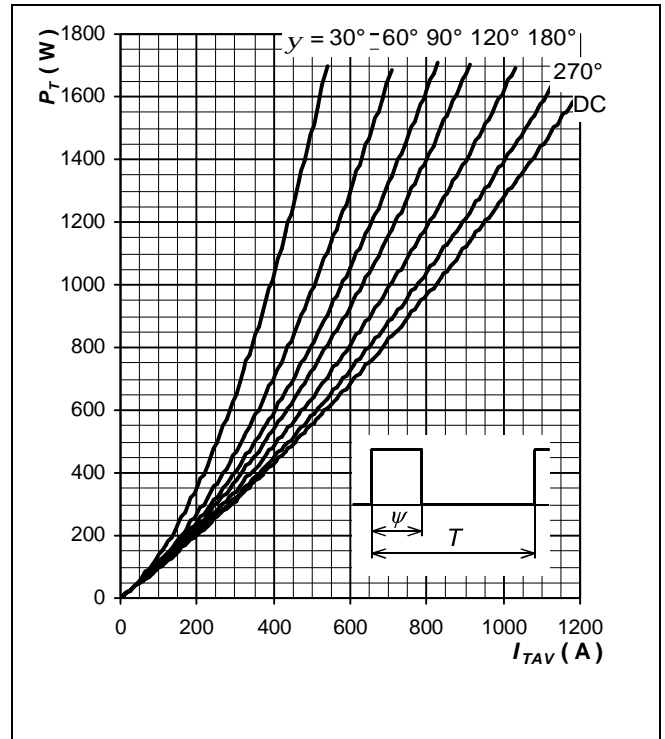


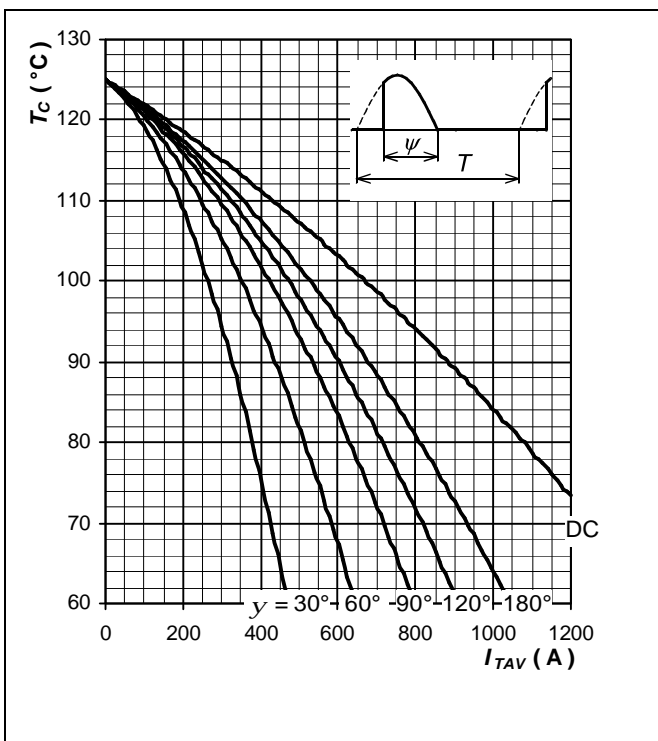
Fig. 5 Gate trigger characteristics



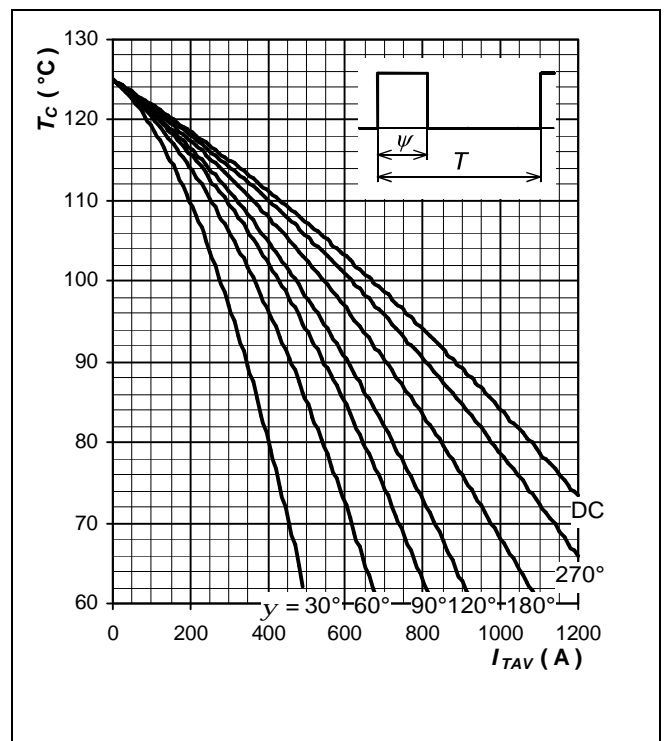
**Fig. 6** Forward power loss vs. average forward current, sine waveform,  $f = 50 \text{ Hz}$ ,  $T = 1/f$



**Fig. 7** Forward power loss vs. average forward current, square waveform,  $f = 50 \text{ Hz}$ ,  $T = 1/f$



**Fig. 8** Max. case temperature vs. average forward current, sine waveform,  $f = 50 \text{ Hz}$ ,  $T = 1/f$



**Fig. 9** Max. case temperature vs. average forward current, square waveform,  $f = 50 \text{ Hz}$ ,  $T = 1/f$

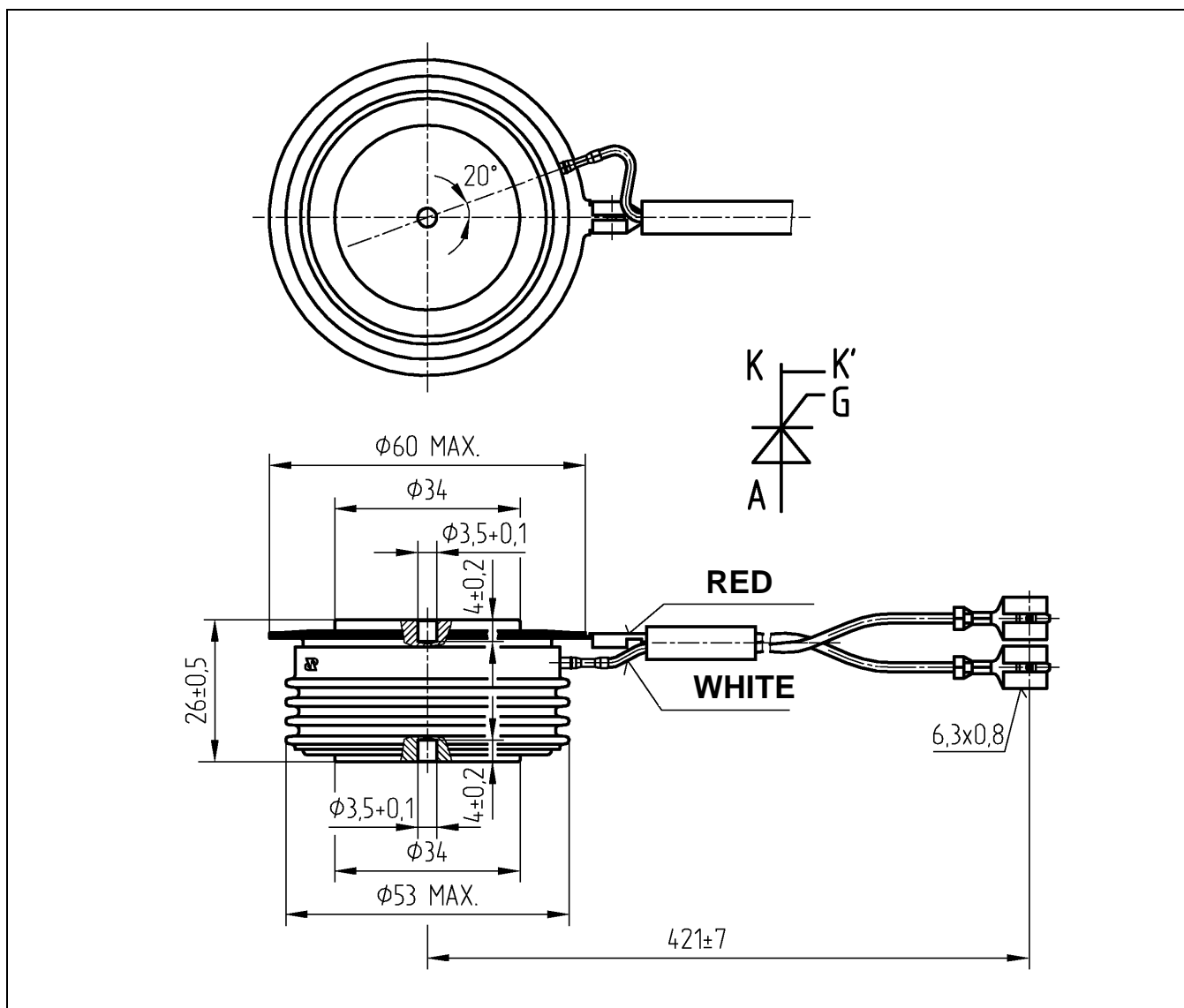


Fig. 10 Device Outline Drawing.

### Related application notes:

Doc. Nr	Titel
5SYA2020	Design of RC-Snubber for Phase Control Applications
5SYA2034	Gate-drive Recommendations for PCT's
5SYA 2036	Recommendations regarding mechanical clamping of Press Pack High Power Semiconductors

Please refer to <http://www.abb.com/semiconductors> for actual versions.

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